

Figure 1

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Integrated  
Circuit 200

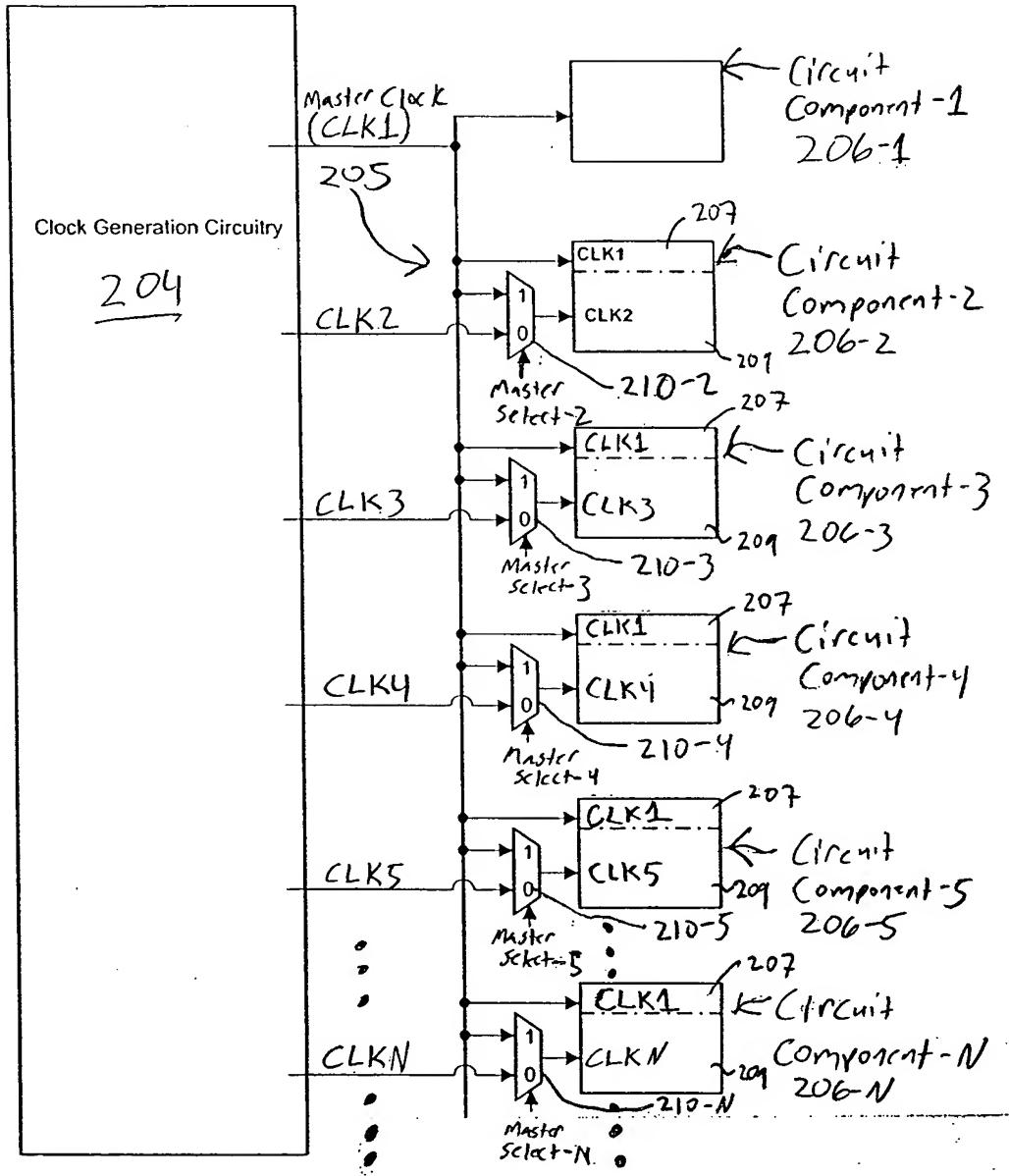
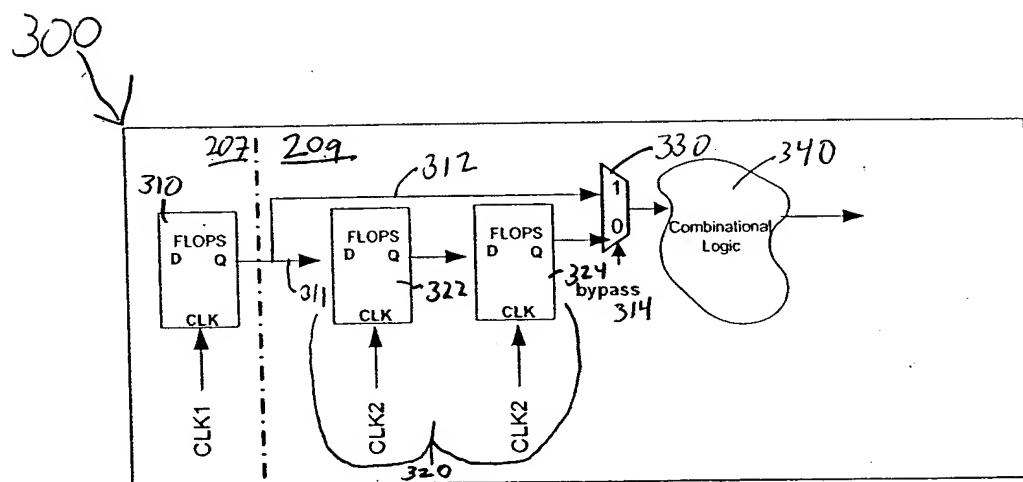


Figure 2 BEST AVAILABLE COPY



Circuit Component - 2  
206-2

Figure 3

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Process for Dynamically Reconfigurable  
Clock Domains on an Integrated Circuit

400

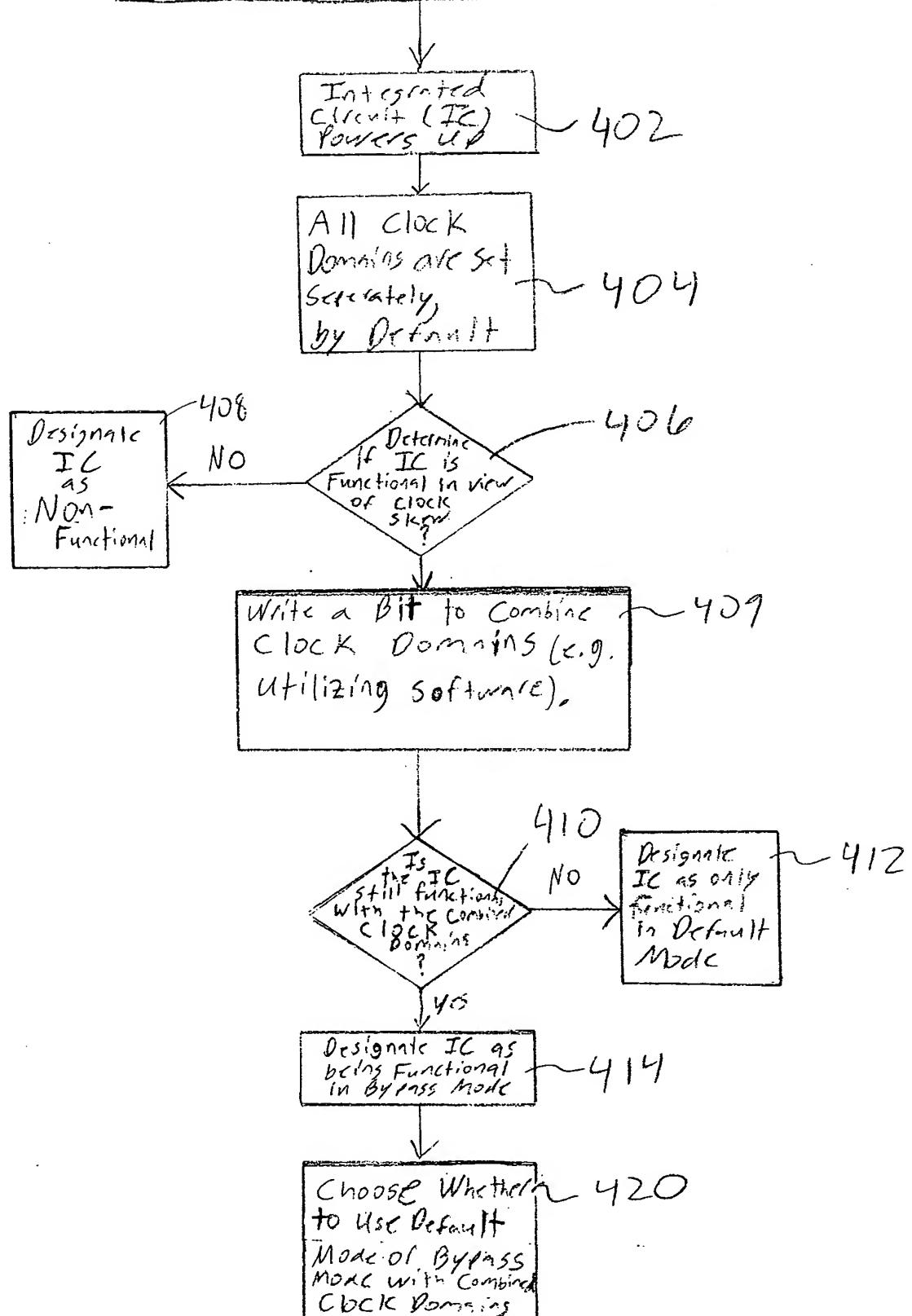


Figure 4

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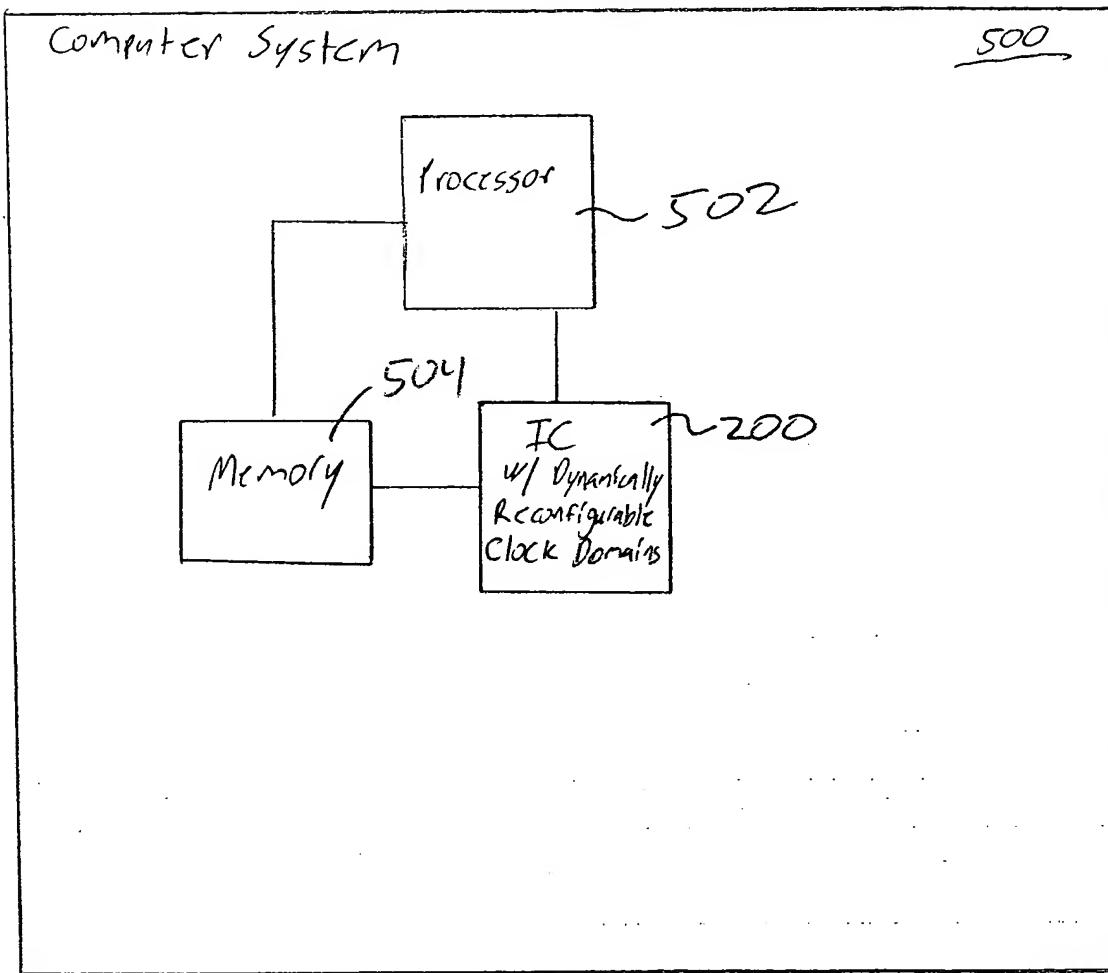


Figure 5

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